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Robin E. Barnum

(Depositor's name)

Robin E. Barnum

(Signature)

January 18, 2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,696	11/08/2001	Jay B. Reimer	TI-30105	7469

TITLE OF INVENTION: MULTICORE DSP DEVICE HAVING COUPLED SUBSYSTEM MEMORY BUSES FOR GLOBAL DMA ACCESS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	04/06/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
DANG, KHANH	2111		710-305000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Robert D. Marshall, Jr.2 W. James Brady, III3 Frederick J. Telecky, Jr.

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Texas Instruments Incorporated

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Dallas, TX USA

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Date January 18, 2005

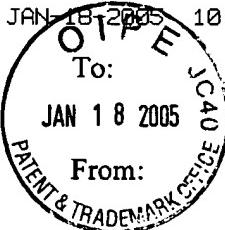
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Robert D. Marshall, Jr.
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30105

Jay B. Reimer, et al.

Art Unit: 2111

Serial No.: 10/008,696

Examiner: Khanh Dang

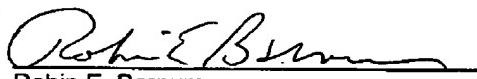
Filed: October 30, 2001

Conf. No.: 7469

For: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access

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NAME OF INVENTOR(S): Jay B. Reimer, et al.		
RECEIPT DATE & SERIAL NO.: Serial No.: 10/008,696		
TITLE OF INVENTION: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access		
TI FILE NO.:	DEPOSIT ACCT. NO.:	
TI-30105	20-0668	
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